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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/JP02/13550 12/25/2002 *AR*

** FOREIGN APPLICATIONS *****

JAPAN 2001-394546 12/26/2001 *AR*

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after grace	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 12
Verified and Acknowledged <i>Examiner's Signature</i> <i>Initials</i>					

ADDRESS

23911

TITLE

Substrate treating method and production method for semiconductor device

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